

100kHz to 1GHz RF Power Detector

May 2002

FEATURES

- Temperature Compensated Internal Schottky Diode RF Detector
- Wide Input Power Range: -34dBm to 14dBm
- Ultra Wide Input Frequency Range: 100kHz to 1000MHz
- Buffered Output
- Wide V_{CC} Range of 2.7V to 6V
- Low Operating Current: 550 μ A
- Low Shutdown Current: <2 μ A
- Low Profile (1mm) ThinSOTTM Package

APPLICATIONS

- Wireless Transceivers
- Wireless and Cable Infrastructure
- RF Power Alarm
- Envelope Detector

DESCRIPTION

The LTC[®]5507 is an RF power detector for applications operating from 100kHz to 1000MHz. The input frequency range is determined by an external capacitor. A temperature-compensated Schottky diode peak detector and buffer amplifier are combined in a small 6-pin ThinSOT package.

The RF input voltage is peak detected using an on-chip Schottky diode and external capacitor. The detected voltage is buffered and supplied to the V_{OUT} pin. A power saving shutdown mode reduces supply current to less than 2 μ A.

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TYPICAL APPLICATION

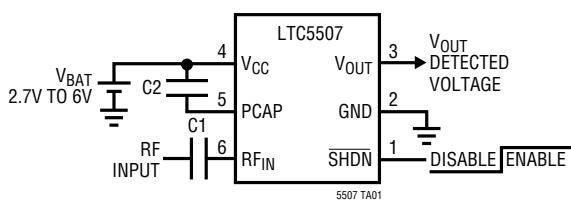
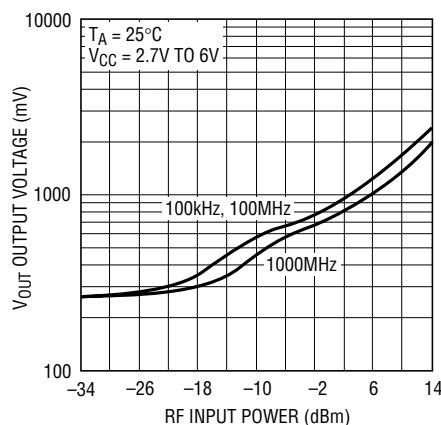


Figure 1. 100kHz to 1000MHz RF Power Detector

Typical Detector Characteristics
at 100kHz, 100MHz and 1000MHz



LTC5507

ABSOLUTE MAXIMUM RATINGS

(Note 1)

| | |
|---|-----------------------------------|
| V _{CC} , V _{OUT} to GND | -0.3V to 6.5V |
| R _{FIN} Voltage to GND | (V _{CC} - 1.8V) to 7V |
| S _H DN Voltage to GND | -0.3V to (V _{CC} + 0.3V) |
| P _{CAP} Voltage to GND | (V _{CC} - 1.8V) to 7V |
| I _{VOUT} | 5mA |
| Operating Temperature Range (Note 2) .. | -40°C to 85°C |
| Maximum Junction Temperature | 125°C |
| Storage Temperature Range | -65°C to 150°C |
| Lead Temperature (Soldering, 10 sec) | 300°C |

PACKAGE/ORDER INFORMATION

| ORDER PART NUMBER |
|-------------------|
| LTC5507ES6 |
| S6 PART MARKING |
| LTZX |

TOP VIEW

S6 PACKAGE
6-LEAD PLASTIC SOT-23
T_{JMAX} = 125°C, θ_{JA} = 250°C/W

Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_{CC} = 3.6V, RF Input Signal is Off, unless otherwise noted.

| PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
|---|--|-----|------|----------|-------------------|
| V _{CC} Operating Voltage | | ● | 2.7 | 6 | V |
| I _{VCC} Shutdown Current | SHDN = 0V | ● | | 2 | μA |
| I _{VCC} Operating Current | SHDN = V _{CC} , I _{VOUT} = 0mA | ● | 0.55 | 0.85 | mA |
| V _{OUT} V _{OOL} (No RF Input) | R _{LOAD} = 2k, SHDN = V _{CC} , Enabled SHDN = 0V, Disabled | | 130 | 250 | mV |
| | | | 1 | 370 | mV |
| V _{OUT} Output Current | V _{OUT} = 1.75V, V _{CC} = 2.7V to 6V, ΔV _{OUT} = 10mV | ● | 1 | 2 | mA |
| V _{OUT} Enable Time | SHDN = V _{CC} , C _{LOAD} = 33pF, R _{LOAD} = 2k | ● | 7 | 20 | μs |
| V _{OUT} Load Capacitance | (Note 4) | ● | | 33 | pF |
| V _{OUT} Noise | V _{CC} = 3V, Noise BW = 1.5MHz, 50Ω RF Input Termination | | 2 | | mV _{P-P} |
| SHDN Voltage, Chip Disabled | V _{CC} = 2.7V to 6V | ● | | 0.35 | V |
| SHDN Voltage, Chip Enabled | V _{CC} = 2.7V to 6V | ● | 1.4 | | V |
| SHDN Input Current | SHDN = 3.6V | ● | 24 | 40 | μA |
| R _{FIN} Input Frequency Range | | | | 0.1–1000 | MHz |
| Max R _{FIN} Input Power | (Note 3) | | | 14 | dBm |
| R _{FIN} AC Input Resistance | F = 10MHz, RF Input = -10dBm F = 1000MHz, RF Input = -10dBm | | | 130 | Ω |
| | | | | 95 | Ω |
| R _{FIN} Input Shunt Capacitance | | | | 1.7 | pF |

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

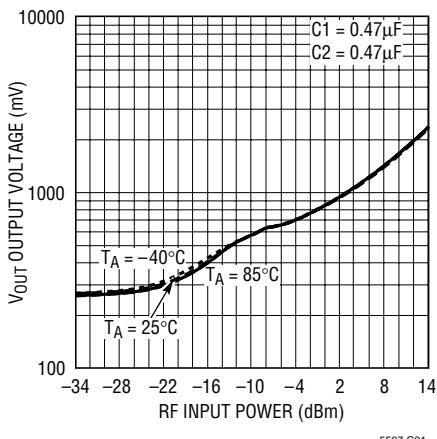
Note 2: Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

Note 3: RF performance is tested at: 80MHz, -4dBm

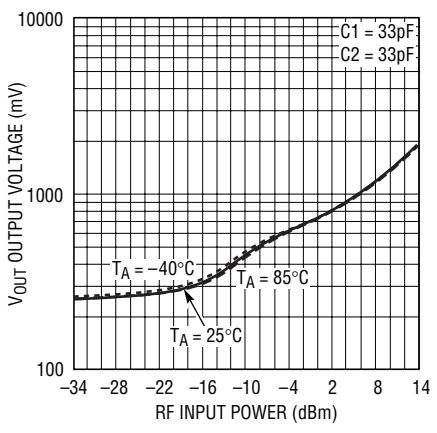
Note 4: Guaranteed by design.

TYPICAL PERFORMANCE CHARACTERISTICS

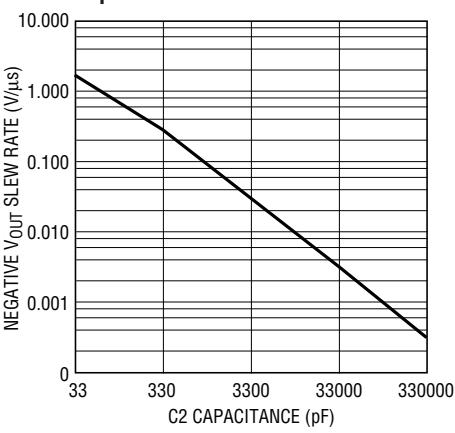
LTC5507 Typical Detector Characteristics, 100kHz, $V_{CC} = 2.7V$ TO $6V$



LTC5507 Typical Detector Characteristics, 100MHz, $V_{CC} = 2.7V$ TO $6V$

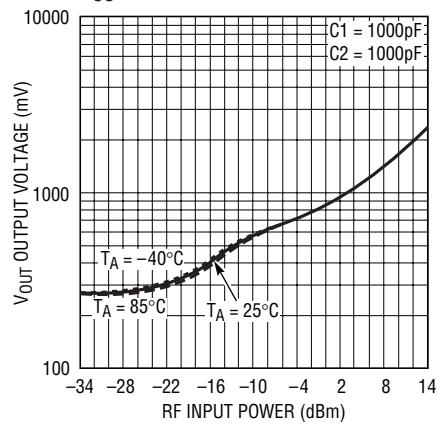


Negative V_{OUT} Slew Rate vs C2 Capacitance



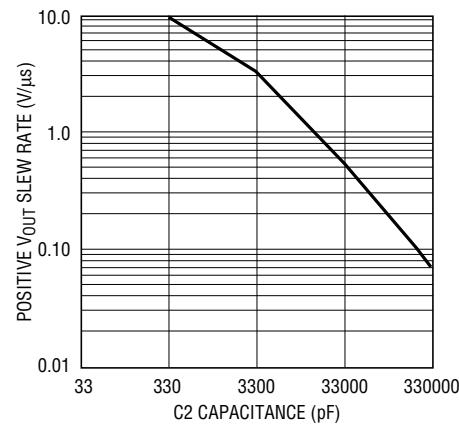
5507 G05

LTC5507 Typical Detector Characteristics, 100MHz, $V_{CC} = 2.7V$ TO $6V$



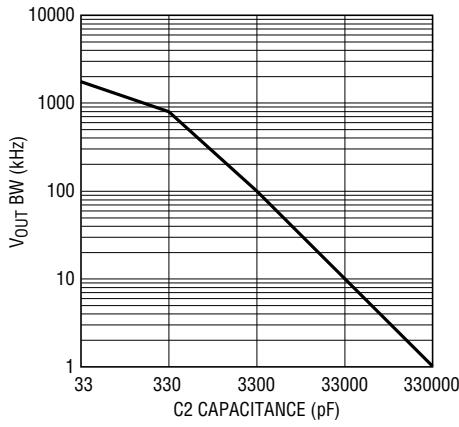
5507 G02

Positive V_{OUT} Slew Rate vs C2 Capacitance



5507 G04

V_{OUT} BW vs C2 Capacitance



5507 G06

LTC5507

PIN FUNCTIONS

SHDN (Pin 1): Shutdown Input. A logic low or no-connect on the SHDN pin places the part in shutdown mode. A logic high enables the part. SHDN has an internal 150k pull down resistor to ensure that the part is in shutdown when the enable driver is in a tri-state condition.

GND (Pin 2): System Ground.

V_{OUT} (Pin 3): Buffered and Level Shifted Detector Output Voltage.

V_{CC} (Pin 4): Power Supply Voltage, 2.7V to 6V. V_{CC} should be bypassed with 0.1μF and 100pF ceramic capacitors.

PCAP (Pin 5): Peak Detector Hold Capacitor. Capacitor value is dependent on RF frequency. Capacitor must be connected between PCAP and V_{CC}.

RF_{IN} (Pin 6): RF Input Voltage. Referenced to V_{CC}. A coupling capacitor must be used to connect to the RF signal source. The frequency range is 100kHz to 1000MHz. This pin has an internal 250Ω termination and an internal Schottky diode detector.

BLOCK DIAGRAM

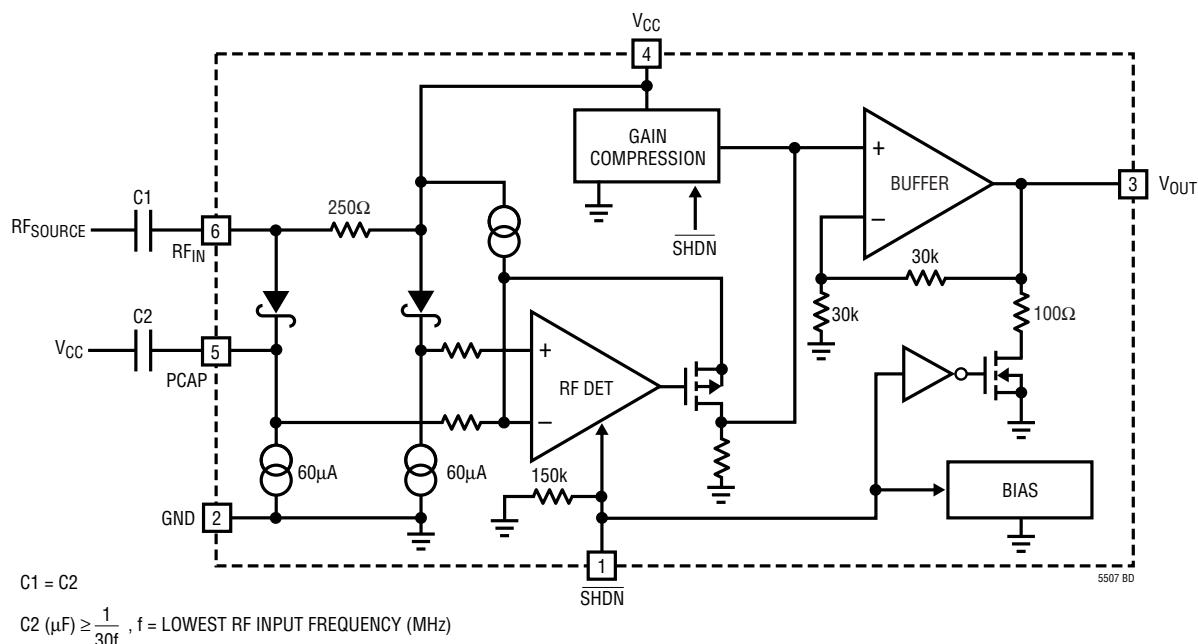


Figure 2.

APPLICATIONS INFORMATION

Operation

The LTC5507 integrates several functions to provide RF power detection over frequencies up to 1000MHz. These functions include an internally compensated buffer amplifier, an RF Schottky diode peak detector and level shift amplifier to convert the RF signal to DC, a delay circuit to avoid voltage transients at V_{OUT} when coming out of shutdown, and a gain compression circuit to extend the detector dynamic range.

Buffer Amplifier

The buffer amplifier has a gain of two and is capable of driving a 2mA load. The buffer amplifier typically has an output voltage range of 0.25V to $V_{CC} - 0.1V$.

RF Detector

The internal RF Schottky diode peak detector and level shift amplifier converts the RF input signal to a low frequency signal. The frequency range of the RF pin is typically up to 1000MHz. The detector demonstrates excellent operation over a wide range of input power. The Schottky detector is biased at about $70\mu A$. The hold capacitor is external.

Gain Compression

The gain compression circuit changes the feedback ratio as the RF peak-detected input voltage increases above 60mV. Below 60mV, the DC voltage gain from the peak detector to the buffer output is 4. Above 140mV, the DC voltage gain is reduced to 0.75. The compression expands the low power detector range due to higher gain.

Modes of Operation

| MODE | SHDN | OPERATION |
|----------|------|--------------|
| Shutdown | Low | Disabled |
| Enable | High | Power Detect |

Applications

The LTC5507 can be used as a self-standing signal strength measuring receiver for a wide range of input signals from -34dBm to 14dBm for frequencies up to 1000MHz.

The LTC5507 can be used as a demodulator for AM and ASK modulated signals with data rates up to 1.5MHz. Depending on specific application needs, the RSSI output can be split into two branches, providing AC-coupled data (or audio) output and DC-coupled, RSSI output for signal strength measurements and AGC.

C1, C2 Capacitor Selection (Refer to Figure 3)

C_1 couples the RF input signal to the detector input RF_{IN} which is referenced to V_{CC} . C_2 is the peak detector capacitor connected between PCAP and V_{CC} . The value of C_2 will affect the slew rate and bandwidth. Typically C_1 can equal C_2 . Ceramic capacitors are recommended for C_1 and C_2 . The values for C_1 and C_2 are dependent on the operating RF frequency. The capacitive reactance should be less than 5Ω to minimize ripple on C_2 .

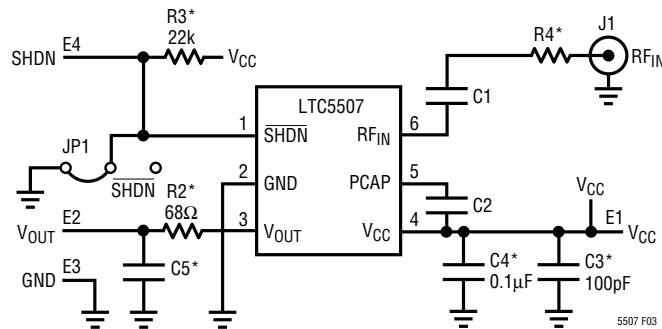
$$C_2(\mu\text{F}) \geq 1/(30 \cdot f) \text{ where } f \text{ is the lowest RF input frequency (MHz)}$$

$$C_1 = C_2$$

In general, select C_1 and C_2 large enough to pass the lowest expected RF signal frequency, as described by the above formulas. But optimize C_1 and C_2 , subject to this constraint, to improve output slew rate and bandwidth, and to enable good AC performance for the highest expected RF signal frequency.

LTC5507

APPLICATIONS INFORMATION

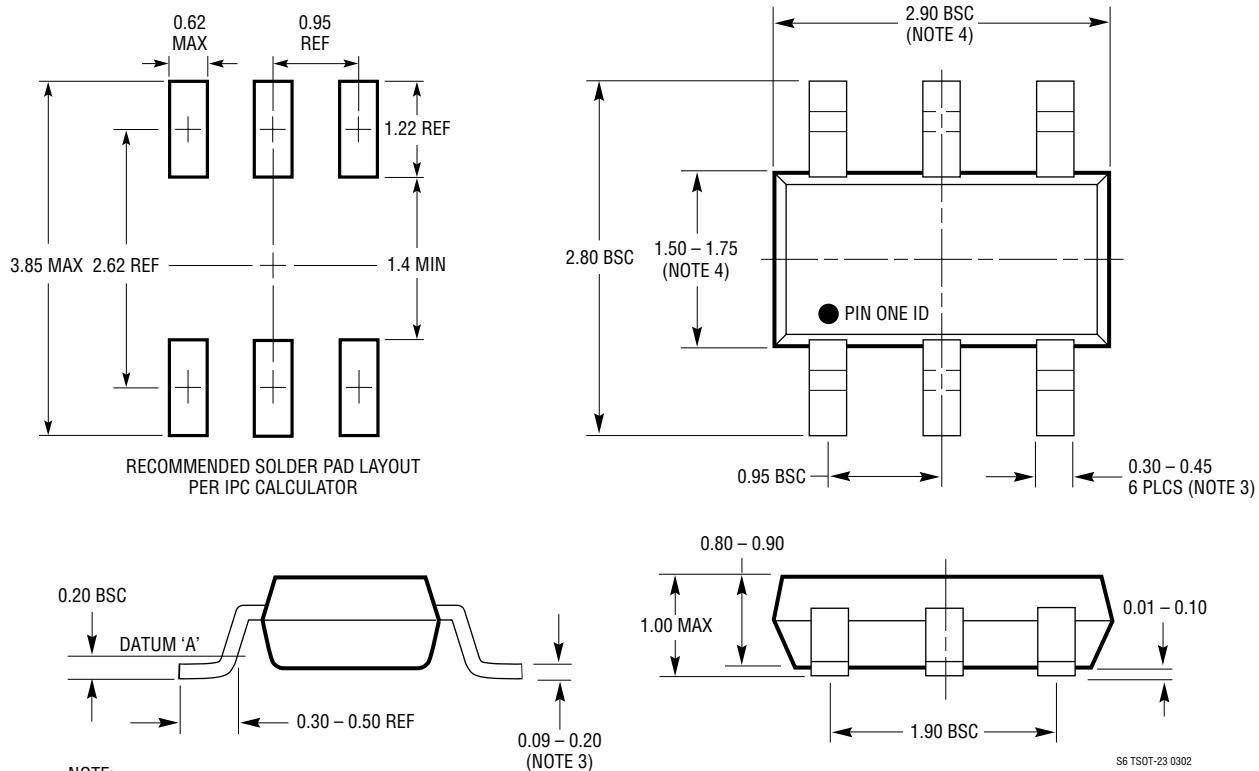


* OPTIONAL COMPONENTS
R2 AND C5 FORM AN OPTIONAL OUTPUT LOWPASS FILTER.
R3 IS USED FOR DEMO PURPOSES ONLY, AND IS NOT USED IN ACTUAL PRODUCT IMPLEMENTATION.
R4 CAN BE USED FOR INPUT POWER LIMITING OR BROADBAND IMPEDANCE MATCHING.
C3 AND C4 ARE OPTIONAL POWER SUPPLY FILTERS.

Figure 3. Evaluation Demo Board Schematic

PACKAGE DESCRIPTION

**S6 Package
6-Lead Plastic TSOT-23**
(Reference LTC DWG # 05-08-1636)



NOTE:

1. DIMENSIONS ARE IN MILLIMETERS
2. DRAWING NOT TO SCALE
3. DIMENSIONS ARE INCLUSIVE OF PLATING
4. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR
5. MOLD FLASH SHALL NOT EXCEED 0.254mm
6. JEDEC PACKAGE REFERENCE IS MO-193

S6 TSOT-23 0302

LTC5507

RELATED PARTS

| PART NUMBER | DESCRIPTION | COMMENTS |
|-----------------------------|---|--|
| RF Power Controllers | | |
| LTC1757A | RF Power Controller | Single/Dual Band GSM/DCS/GPRS Mobile Phones |
| LTC1758 | RF Power Controller | Single/Dual Band GSM/DCS/GPRS Mobile Phones |
| LTC1957 | RF Power Controller | Single/Dual Band GSM/DCS/GPRS Mobile Phones |
| LTC4400 | ThinSOT RF PA Controller | Single/Dual Band GSM/DCS/GPRS Phones, 45dB Dynamic Range, 450kHz Loop BW |
| LTC4401 | ThinSOT RF PA Controller | Single/Dual Band GSM/DCS/GPRS Phones, 45dB Dynamic Range, 250kHz Loop BW |
| LT5504 | 800MHz to 2.7GHz RF Measuring Receivers | 80dB Dynamic Range |
| LTC5505 | ThinSOT RF Power Detector | 300MHz to 3GHz RF Power Detector |
| Other Related Parts | | |
| LTC1503 | Inductorless Step-Down DC/DC Converter | 600kHz, Up to 100mA, 25% Higher Efficiency than Linear Regulator |
| LTC1555L-1.8 | SIM Power Supply and Level Translator | Generates 1.8V, 3V or 5V; >10kV ESD on All SIM Contact Pins |
| LT®1615 | Step-Up DC/DC Converter | ThinSOT, Low 20µA Quiescent Current, V_{IN} as Low as 1V, 300mA I_{OUT} |
| LT1617 | Inverting DC/DC Converter | ThinSOT, Low 20µA Quiescent Current, V_{IN} as Low as 1V, 300mA I_{OUT} |
| LTC1682 | Low Noise Charge Pump with LDO | 60µV _{RMS} Output Noise, Small MSOP Package |
| LTC1734 | ThinSOT Li-Ion Battery Charger | Only Two External Components, Charge Current Indicator, up to 700mA |
| LT1761 | Low Dropout, Low Noise Linear Regulator | ThinSOT, 300mV Dropout at 100mA, 20µV _{RMS} Output Noise (10Hz to 100kHz) |
| LTC1878 | Step-Down DC/DC Converter | Integrated Synchronous Operation, Up to 95% Efficiency, 1A Switch Current |
| LTC1928 | Low Noise Charge Pump | ThinSOT, 90µV _{RMS} Output Noise (100kHz BW), I_{OUT} Up to 30mA |
| LT1932 | White LED Driver | ThinSOT, 1.2MHz DC/DC Constant-Current LED Driver, Dimming Control |
| LT1944 | Step-Up DC/DC Converter | Dual Output for LCD Bias, Low Quiescent Current of 20µA, $1.2V \leq V_{IN} \leq 15V$ |
| LTC1986 | SIM Power Supply | ThinSOT, 3V and 5V, Ultralow Supply Current of 14µA, <0.92cm ² PCB |
| LTC3200 | Low Noise Charge Pump | 2MHz Switching Frequency Allows Small Size Capacitors, I_{OUT} Up to 100mA |
| LTC3401 | Step-Up DC/DC Converter | Synchronous Rectification, Up to 97% Efficiency, 1A Switch Current, 3MHz |
| LTC3402 | Step-Up DC/DC Converter | Synchronous Rectification, Up to 97% Efficiency, 2A Switch Current, 3MHz |
| LTC3404 | Step-Down DC/DC Converter | 1.4MHz Synchronous Rectification, 10µA Quiescent Current |
| LT5511 | High Signal Level Upconverting Mixer | 10MHz to 3000MHz RF Output Range, Integrated LO Buffer, 17dBm IIP3 |

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